AGILENT TECHNOLOGIES Legal Department, M/S DL429 ntellectual Property Administration **e**x. Box 7599 eland. CO 80537-0599

ORIGINALLY FILED

IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S): Que-Won Rhee

RECEIVED

CASE: 10991663-1

SIR:

GROUP ART UNIT: 2182

AUG 2 2 2002

SERIAL NO: 09/432,819 FILED: November 2, 1999

EXAMINER: Ilwoo Park.

Technology Center 2100

7VIII-11354 ---SUBJECT: CONFIGURABLE ARCHITECTURE FOR VIRTUAL SOCKET CLIENT TO

AN ON-CHIP BUS INTERFACE BLOCK

ASSISTANT COMMISSIONER OF PATENTS WASHINGTON, D.C. 20231

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

| Transmitte | d here | with are th | e fol | lowing | in the | abovė-io | dent | ifiéd | appl | iĉa | tion: | \$ 7 | si⊊ , , , | |
|--|--|--|----------------------------------|------------------------------|----------------|-------------------------------------|----------------------------------|---------------------------------|----------------------------------|-------------------------|--------------------------------|-------------|---------------|-------------------------|
| Other From Promay be mate accordance v | ee as c TO 1449 rial to the with 37 C | alculated be together we examination FR 1.56 und | vith _ n of there is a second | referents application CFR 1. | [] ences o | Shee Shee Ind for which | et Fo et In lican h the | orma form ts are re ma | I Dra al Dr awar y be a | wir aw e, w du | ngs ings hich ty to d | app disc | licar lose | in |
| CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY | | | | | | | | | | | | | | |
| (1) | | (2) REMAINING AMENDMENT | | (3) | | (4) SHEST NUMBE TOUSLY PAID I | | PRE | 5) SENT TRA | . ** . | (6) RATE | | | (7) DITIONAL FEES |
| TOTAL CLAIMS | | | MII | NUS | | | : | = | 0 | X | \$18 | | \$ | |
| INDEP. CLAIMS | | | MII | NUS | | | | = | | X | \$84 | • | \$ | - |
| [] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM \$280 \$ | | | | | | | | | | | | | | |
| EXTENSION FEE | | 1ST MONTH \$110 [] | | 2ND MC \$400 | | 3RD MON \$920 | | | 4TH MON \$1,440 | | | | \$ | |
| TOTAL ADDITIONAL FEE \$ FOR THIS AMENDMENT | | | | | | | | | | | | | | |

_ to Deposit Account 50-1078. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 50-1078 pursuant to 37 CFR 1.25. Additionally please charge any fee to Deposit Account 50-1078 under 37 CFR 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231.

Date of Deposit: July 18, 2002

By: Douglas L. Welle

Signature:

Respectfully Submitte

Douglas L. Weller **Attorney for Applicant** Reg. No. 30,506

Date: July 17, 2002

Telephone: (408) 985-0642

COPY OF PAPE

IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

INTOR(S): Que-Won Rhee

CASE: 10991663-1

RECEIVED

SERIAL NO: 09/432,819

GROUP ART UNIT: 2182

AUG 2 2 2002

FILED: November 2, 1999

EXAMINER: Ilwoo Park

Technology Center 2100

SUBJECT: CONFIGURABLE ARCHITECTURE FOR VIRTUAL

SOCKET CLIENT TO AN ON-CHIP BUS INTERFACE

ASSISTANT COMMISSIONER OF PATENTS WASHINGTON, D.C. 20231

SIR:

AMENDMENT A

In response to the Office Action dated April 23, 2002, please amend the above-identified patent application as follows:

In the Specification:

Amend the three paragraphs found at page 5, line 20 through page 6, line 24 as follows:

The second stage of the configurable architecture for interface block 19 is implemented as a translation block 32. A clocked buffer 36 receives and transmits control signals from/to synchronization block 31 via control lines 41 and receives and transmits data signals from/to synchronization block 31 via data lines 46. Clocked buffer 36 receives and transmits control signals from/to translation block 32 via control lines 51 and receives and transmits data signals from/to translation block 32 via data lines 46. Translation block 32 converts the block encoding used by the virtual socket interface protocol of specialized logic block 10 to the block encoding used by